


Serial No. 09/805,137
Docket No. BUR998050DIV2

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AMENDMENTS TO THE SPECIFICATION

Please amend the paragraph beginning on page 1, line 4 as follows:

 The present Application is a Divisional Application of U.S. Patent Application No.
~~09/128,503~~ 6,237,101 filed on August 3, 1998.

AMENDMENTS TO THE DRAWING FIGURES

With respect to Figs. 1 and 2, Applicants respectfully traverse the designation of a legend such as --Prior Art-- because that which is shown, was not published and was only known to the Applicants and others employed by the common Assignee at the time of the invention.

With respect to Fig. 3A, Applicants respectfully submit that the outputs of the OR TREE LOGIC 33, "TO OTHER UNITS" and "TO OTHER PIPELINE STAGES," are supported by the Application at page 7, line 28 to page 8, line 1, "While exemplarily there are five microcode units shown in Fig. 3A, many more (or less depending on the design) may be provided." Thus, to one of ordinary skill in the art, "TO OTHER UNITS" would indicate outputs to other microcode units, while "TO OTHER PIPELINE STAGES" would indicate outputs to pipelines, which are well known to those of ordinary skill in the art, as structures for transmitting data.

With respect to Fig. 3B, the Application clearly states "[t]he plurality of outputs from AND gates of each of the microcode subunits 321, 322, etc. are input to respective ones of internally or externally provided OR gate(s) 323A, 323B, 323C, etc. (or directly to the OR tree 33, shown in Figure 3A, depending upon the number of outputs to be OR'd) which ORs the inputs to provide a single output from the microcode unit 32." Thus, the output of Fig. 3B represents the output of the microcode unit 32, shown in Fig. 3A, which is readily apparent to one of ordinary skill in the art.

Attached is an "Annotated Sheet Showing Changes" and a "Replacement Sheet for Figs. 4A and 4B, which clearly indicate the input to S48 of Fig. 4B, from the S42-DONE output of Fig. 4A.